

SHEET 1 OF 3

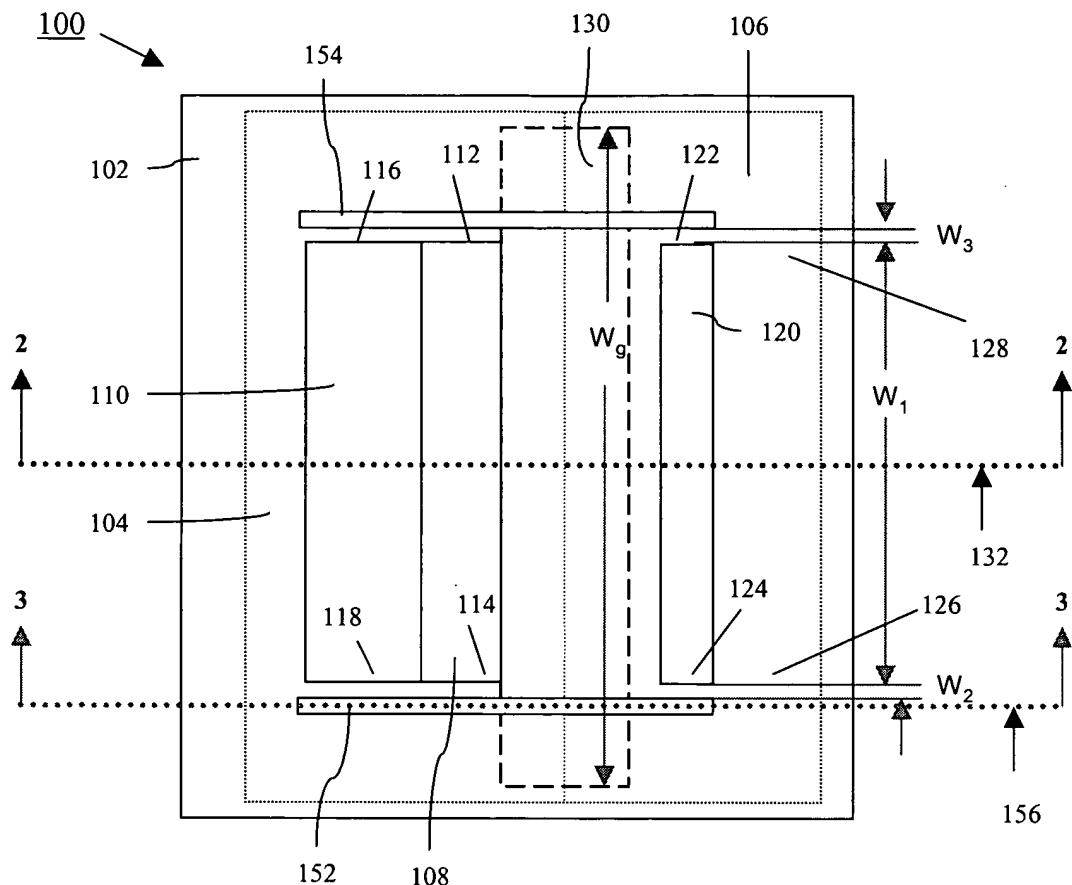


Fig. 1

Attorney Docket No. 24061.148
UTILIZE DEEP TRENCH STRUCTURE TO PREVENT LDMOS FROM PARASITIC TUNING-ON CHANNELS

SHEET 2 OF 3

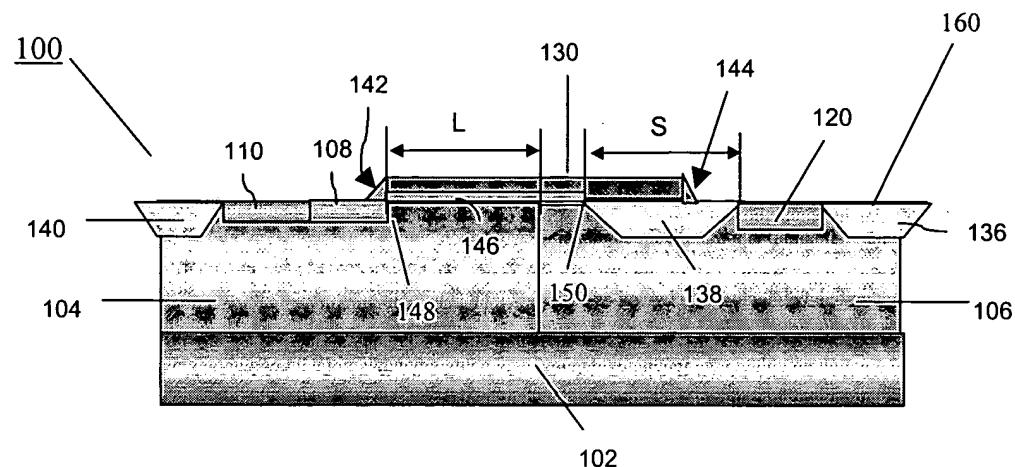


Fig. 2

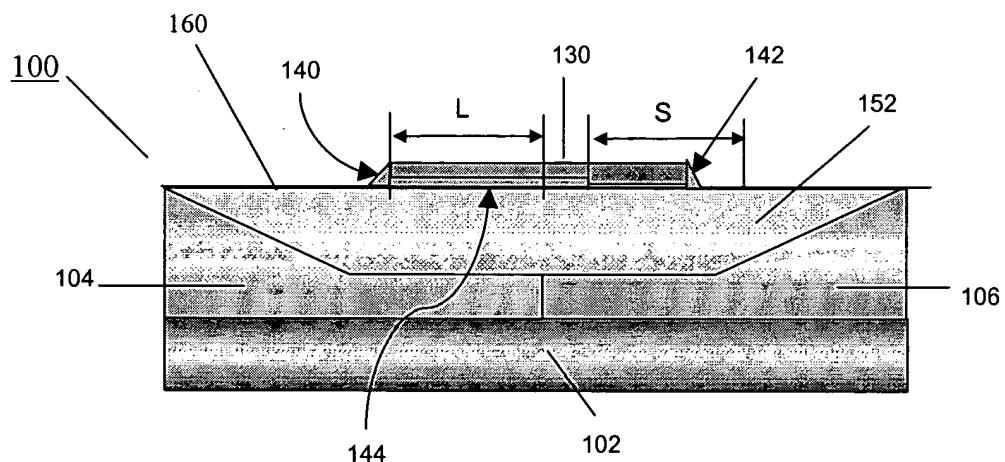


Fig. 3

Attorney Docket No. 24061.148
UTILIZE DEEP TRENCH STRUCTURE TO PREVENT LDMOS FROM PARASITIC TUNING-ON CHANNELS

SHEET 3 OF 3

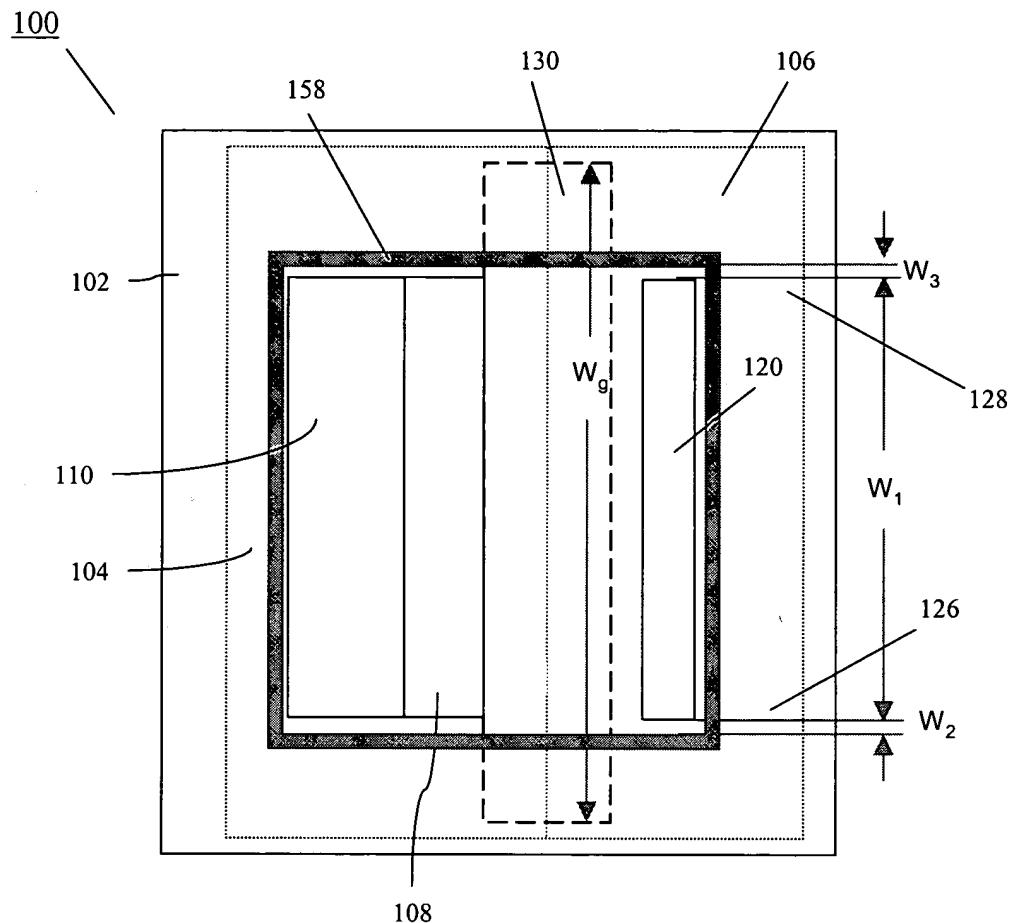


Fig. 4